

PTO/SB/21 (09-04)

Approved for use through 07/31/2006. OMB 0651-0031
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**TRANSMITTAL
FORM**

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

Application Number	10/625,068
Filing Date	July 22, 2003
First Named Inventor	Arup Bhattacharyya
Art Unit	2829
Examiner Name	Christian D. Wilson
Attorney Docket Number	MI22-2362

6908798

ENCLOSURES (Check all that apply)

- | | | |
|--|--|---|
| <input checked="" type="checkbox"/> Fee Transmittal Form
<input checked="" type="checkbox"/> Fee Attached
<input type="checkbox"/> Amendment/Reply
<input type="checkbox"/> After Final
<input type="checkbox"/> Affidavits/declaration(s)
<input type="checkbox"/> Extension of Time Request
<input type="checkbox"/> Express Abandonment Request
<input type="checkbox"/> Information Disclosure Statement

<input type="checkbox"/> Certified Copy of Priority Document(s)
<input type="checkbox"/> Reply to Missing Parts/
Incomplete Application
<input type="checkbox"/> Reply to Missing Parts
under 37 CFR 1.52 or 1.53 | <input type="checkbox"/> Drawing(s)
<input type="checkbox"/> Licensing-related Papers

<input type="checkbox"/> Petition
<input type="checkbox"/> Petition to Convert to a
Provisional Application
<input type="checkbox"/> Power of Attorney, Revocation
Change of Correspondence Address
<input type="checkbox"/> Terminal Disclaimer
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<input type="checkbox"/> CD, Number of CD(s) _____
<input type="checkbox"/> Landscape Table on CD | <input type="checkbox"/> After Allowance Communication to TC

<input type="checkbox"/> Appeal Communication to Board
of Appeals and Interferences

<input type="checkbox"/> Appeal Communication to TC
(Appeal Notice, Brief, Reply Brief)

<input type="checkbox"/> Proprietary Information

<input type="checkbox"/> Status Letter
<input checked="" type="checkbox"/> Other Enclosure(s) (please identify
below):
Return Receipt Postcard; Check - \$100.00;
Request for Certificate of Correction;
Certificate of Correction |
|--|--|---|

RemarksU.S. Patent No. 6,908,798
Issued: June 21, 2005**Certificate**
MAY 26 2006**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT of Correction**

Firm Name	Wells St. John P.S.		
Signature			
Printed name	David G. Latwesen, Ph.D.		
Date	5/22/06	Reg. No.	38,533

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:

Signature			
Typed or printed name	Terri Lynn Boyd-Davis	Date	5-22-06

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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MAY 26 2006

MAY 24 2006

PTO/SB/17 (01-06)

Approved for use through 07/31/2006. OMB 0651-0032

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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pursuant to the Consolidated Appropriations Act, 2005 (H.R. 4818).

FEE TRANSMITTAL
For FY 2006☐ Applicant claims small entity status. See 37 CFR 1.27

TOTAL AMOUNT OF PAYMENT (\$) 100.00

Complete if Known

Application Number	10/625,068
Filing Date	July 22, 2003
First Named Inventor	Arup Bhattacharyya
Examiner Name	Christian D. Wilson
Art Unit	2829
Attorney Docket No.	MI22-2362

METHOD OF PAYMENT (check all that apply)☒ Check ☐ Credit Card ☐ Money Order ☐ None ☐ Other (please identify): _____☒ Deposit Account Deposit Account Number: 23-0925 Deposit Account Name: Wells St. John P.S.

For the above-identified deposit account, the Director is hereby authorized to: (check all that apply)

☐ Charge fee(s) indicated below☐ Charge fee(s) indicated below, except for the filing fee☒ Charge any additional fee(s) or underpayments of fee(s) under 37 CFR 1.16 and 1.17☒ Credit any overpayments

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

FEE CALCULATION (All the fees below are due upon filing or may be subject to a surcharge.)**1. BASIC FILING, SEARCH, AND EXAMINATION FEES**

Application Type	FILING FEES		SEARCH FEES		EXAMINATION FEES		Fees Paid (\$)
	Fee (\$)	Small Entity Fee (\$)	Fee (\$)	Small Entity Fee (\$)	Fee (\$)	Small Entity Fee (\$)	
Utility	300	150	500	250	200	100	
Design	200	100	100	50	130	65	
Plant	200	100	300	150	160	80	
Reissue	300	150	500	250	600	300	
Provisional	200	100	0	0	0	0	

2. EXCESS CLAIM FEES

Fee Description	Fee (\$)	Small Entity Fee (\$)
Each claim over 20 (including Reissues)	50	25
Each independent claim over 3 (including Reissues)	200	100
Multiple dependent claims	360	180

Total Claims	Extra Claims	Fee (\$)	Fee Paid (\$)	Multiple Dependent Claims	Fee (\$)	Fee Paid (\$)
_____ - 20 or HP = _____	x _____	= _____				
HP = highest number of total claims paid for, if greater than 20.						
Indep. Claims	Extra Claims	Fee (\$)	Fee Paid (\$)			
_____ - 3 or HP = _____	x _____	= _____				
HP = highest number of independent claims paid for, if greater than 3.						

3. APPLICATION SIZE FEE

If the specification and drawings exceed 100 sheets of paper (excluding electronically filed sequence or computer listings under 37 CFR 1.52(e)), the application size fee due is \$250 (\$125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).

Total Sheets	Extra Sheets	Number of each additional 50 or fraction thereof	Fee (\$)	Fee Paid (\$)
_____ - 100 = _____	/ 50 = _____	(round up to a whole number) x _____	= _____	

4. OTHER FEE(S)

Non-English Specification, \$130 fee (no small entity discount)

Other (e.g., late filing surcharge): Certificate of Correction

Fees Paid (\$)

100.00

SUBMITTED BY

Signature		Registration No. (Attorney/Agent) 38,533	Telephone (509) 624-4276
Name (Print/Type)	David G. Latwesen, Ph.D.	Date	5/22/06

This collection of information is required by 37 CFR 1.136. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 30 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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MAY 26 2006



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No. 6,908,798 B1
Patent Issue Date June 21, 2005
Application Serial No. 10/625,068
Filing Date July 22, 2003
Assignee Micron Technology, Inc.
Inventorship Arup Bhattacharyya
Attorney's Docket No. MI22-2362
Title: **"Methods of Making Semiconductor-On-Insulator Thin Film Transistor
Constructions"**

**REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT FOR
APPLICANT MISTAKES and PTO MISTAKES (37 C.F.R. §§ 1.322(a) and 1.323)**

To: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTN: Decision and Certificate of Correction
Branch of the Patent Issue Division

From: David G. Latwesen, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

Sir:

It is hereby requested that a Certificate of Correction be issued with respect to Patent No. 6,908,798 B1, granted June 21, 2005, in accordance with the Certificate of Correction form attached hereto in duplicate.

It is noted that errors appear in this patent of a typographical nature of character, as more fully described below. The errors occurred in good faith. Correction thereof does not involve such changes in the patent as would constitute new matter or would require re-examination.

05/25/2006 EAREGAY1 00000068 6908798

01 FC:1811

100.00 OP

Other errors listed on the Certificate of Correction form were apparently incurred through the fault of the PTO as will be disclosed by the records of files in the Office.

Attached hereto, in duplicate, is Form PTO-1050, with at least one copy being suitable for printing.


The exact page and line number where the errors occur in the application file are:

- Page 5, paragraph [0009], line 10 in the Specification/Col. 3, line 22 in the issued patent;
- Page 7, paragraph [0016], line 1 in the Specification/Col. 4, line 20 in the issued patent.

Enclosed is a check in the amount of \$100.00, as required by 37 CFR 1.20(a).

Respectfully submitted,

Dated: 5/22/06

By: 
David G. Latwesen, Ph.D.
Reg. No. 38,533

MAY 26 2006

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO. : 6,908,798 B1

APPLICATION NO.: 10/625,068

ISSUE DATE : June 21, 2005

INVENTOR(S) : Arup Bhattacharyya

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the References Cited:

Col. 2, line 14 -

Replace "Kim, C.H. et al., "A New High -Performance Poly3-Si TFT"

With --Kim, C.H. et al, "A New High -Performance Poly-Si TFT--

In the Specification:

Col. 2, line 44 -

Replace ""High Performance Sub-100 nm Si TFT by Pattem--

With --"High Performance Sub-100 nm Si TFT by Pattern---

Col. 2, line 54 -

Replace "islands for device channel regions. Nickelinduced-lateral"

With --islands for device channel regions. Nickel-induced-lateral---

Col. 3, line 22 -

Replace "Digest, 2002, 00. 98-99; and Huang, L. J. et al.,"

With --Digest, 2002, pp. 98-99; and Huang, L. J. et al.,--

Col. 4, line 20 -

Replace "FIG. 2 is a view of the FIG. 1 wafer shown at a processing"

With --FIG. 2 is a view of the FIG. 1 fragment shown at a processing--

Col. 10, line 66 -

Replace "similar numbering will be used as is used above In"

With --similar numbering will be used as is used above in--

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Wells St. John P.S.
601 W. 1st Avenue, Suite 1300
Spokane, WA 99201

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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MAY 23 2006

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO. : 6,908,798 B1

APPLICATION NO.: 10/625,068

ISSUE DATE : June 21, 2005

INVENTOR(S) : Arup Bhattacharyya

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With --Kim, C.H. et al, "A New High -Performance Poly-Si TFT--

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Replace ""High Performance Sub-100 nm Si TFT by Pattern-"
With --"High Performance Sub-100 nm Si TFT by Pattern---

Col. 2, line 54 -

Replace "islands for device channel regions. Nickelinduced-lateral"
With --islands for device channel regions. Nickel-induced-lateral---

Col. 3, line 22 -

Replace "Digest, 2002, 00. 98-99; and Huang, L. J. et al.,"
With --Digest, 2002, pp. 98-99; and Huang, L. J. et al.,--

Col. 4, line 20 -

Replace "FIG. 2 is a view of the FIG. 1 wafer shown at a processing"
With --FIG. 2 is a view of the FIG. 1 fragment shown at a processing--

Col. 10, line 66 -

Replace "similar numbering will be used as is used above In"
With --similar numbering will be used as is used above in--

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Spokane, WA 99201

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MAY 29 2006